

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Mark G. Johnson et al.

Title:

INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL

MEMORY ARRAY WITH DUAL OPPOSING DECODER

ARRANGEMENT

Application No.: 10/774,818

Filed:

February 9, 2004

Examiner:

Unknown

Group Art Unit:

2812

Atty. Docket No.: 023-0025

Confirmation No.:

9232

June 3, 2005

Mail Stop Amendment **COMMISSIONER FOR PATENTS** P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT 37 C.F.R. § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

\boxtimes	Form(s) PTO/SB/08A	(1	page(s),	with	copy of no	refe	rence(s)).
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Form(s) PTO/SB/08B (1 page(s), with copy of 1 reference(s)).

Listing of Pending Applications (N/A page(s), with copy of no application(s)).

Other: N/A

Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

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	Certain information was submitted to, or cited by, the Office in Application No(s). N/A, filed N/A, to which the above-identified application claims priority under 35 U.S.C. § 120. In accordance with 37 C.F.R. § 1.98(d), copies of that information are not included.
	Pursuant to 1287 OG 163 (October 19, 2004), for those cited pending U.S. patent applications filed (or which entered the national stage under 35 U.S.C. § 371) on or after June 30, 2003 or which are otherwise stored in the USPTO's Image File Wrapper system, copies are not included.
	FEE AUTHORIZATION
	This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
	The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
	If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

I hereby certify that, on the date shown below, this correspondence is being
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deposited with the US Postal Service with sufficient postage as first class mail and addressed as shown above. ☐ facsipme transmitted to the US Parent and Trademark Office. Andrew C. Graham Date

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Respectfully submitted,

Andrew C. Graham, Reg. No. 36,531

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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0025
	Application No.: 10/774,818
INFORMATION DISCLOSURE STATEMENT BY APPLICAN	First Named Inventor: Mark Johnson
OIPE	Filing Date: February 9, 2004
	Group Art Unit: 28/2
JUN 0 6 2005 E	Examiner Name: Unknown
Sheet I of I	Date Submitted: June 3, 2005

			U.S. PATENT DO	CUMENTS			
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	AA	5,691,933	11-25-1997	Takenaka			
	AB	6,055,180	04-25-2000	Gudesen et al.			
	AC	6,324,093	11-27-2001	Perner et al.			
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		1	FOREIGN PATENT	DOCUMENTS			
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶	
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PTO/SB/08B	Substitu	te for form 1449/PTO)		Attorney Docket No.:	023-0025
				Application No.:	10/774,818
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			First Named Inventor:	Mark Johnson	
1				Filing Date:	February 9, 2004
	Ĺ	0 6 2005		Group Art Unit:	2812
	JUN	3	1	Examiner Name:	Unknown
Sheet 1 of 1	TEN	A TRADEMA	I	Date Submitted:	June 3, 2005
			LITERATURE DOCUMEN	TS	
Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL (book, magazine, journal, serial, syn publisher, o		, page(s), volume-issue	
-	AA	SAEKI ET AL., "A 2.5-ns Clock Access, 2 Journal of Solid-State Circuits, Vol. 31, N	250-MHz, 256-Mb SDRAM o. 11, November 1996, pp. 10	with Synchronous Mirro 556-1668	or Delay," IEEE
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Examiner Signature			Date Considered		<u> </u>

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Applicant's unique citation designation number (optional).

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